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Inventor : Yoshinobu Kaneyama
Title : DEVICE MOUNTING METHOD
Assignee : NEC Corporation

Enclosed herewith please find the following documents in the above-identified application for United States Letters Patent:

14 Pages of Specification including Abstract and Claims
19 Numbered Claims Calculated as 19 Claims for Fee Purposes
5 Sheets of Drawing Containing Figures 1A to 5. Formal
1 Declaration and Power of Attorney
X Priority is Claimed under 35 U.S.C. §119:
Convention Date November 27, 1997 for JAPAN Appln. S.N. 326154/1997
X Certified Priority Application
-- Verified Statement Claiming Small Entity Status under 37 C.F.R. §1.27.
1 Assignment & Conveyance
1 PTO 1449
1 Return-Addressed Post Card

OFGS Check #81080, which includes the fee of \$800.00, calculated as follows:
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November 24, 1998

Date of Signature

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DEVICE MOUNTING METHOD

BACKGROUND OF THE INVENTION

5 1. Field of the Invention

The present invention relates to a method of mounting a device such as an optical device or the like, and particularly to a method of positioning and mounting a device such as an optical device or the like to a substrate by using solder bumps.

10 2. Description of the Related Art

As disclosed in TECHNICAL REPORT IEICE, OQE93-145 (1993-12), such a method of mounting the optical device on the substrate has been used to fix the optical device with high positioning precision in an optical communication module.

15 Figs. 1A to 1C and Figs. 2A to 2C are diagrams showing a series of steps for a conventional method of mounting an optical device.

A circular substrate electrodes 6 of 50 μm in diameter are formed on the surface of a silicon substrate 5 to join an
20 optical device 13 of 300 μm square to the surface of the silicon substrate 5. A die 1 and a punch 2 are used to punch a solder sheet 3 to thereby obtain solder pieces 4. A heater 8 is embedded in a heater table 16, and a flux 15 is used to remove an oxide film on the surface of the solder pieces 4 when the soldering
25 pieces 4 are melted. The solder pieces 4 are melted and the shape thereof is varied, thereby forming solder bumps 12. Circular optical device electrodes 14 of 50 μm in diameter which are formed on the optical device 13 are joined to the substrate electrodes 6 through the solder bumps 12.

30 Next, the operation of each step will be described.

As shown in Fig. 1A, the solder pieces 4 which are punched out from the solder sheet 3 by the die 1 and the punch 2 are bumped against the substrate electrodes 6 formed on the silicon substrate 5. Subsequently, as shown in Fig. 1B, the silicon substrate 5 against which the solder pieces 4 are bumped is mounted on the heater table 16 in which the heater 8 is embedded. The flux 15 is coated on the solder pieces 4. Thereafter, the solder pieces 4 are heated by the heater 8 to be melted, and the oxide films on the surface of the soldering pieces are removed by the flux 15, thereby forming the solder bumps 12 under surface tension as shown in Fig. 1C. Thereafter, the substrate 5 is washed to remove the residues of the flux 12. The substrate from which the residues of the flux 12 are removed is mounted on the heater table 16 again as shown in Fig. 2A. Thereafter, the optical device 13 is temporarily mounted so that the optical device electrodes 14 formed on the optical device are positioned onto the solder bumps 12. At this time, as shown in Fig. 2A, the center of each substrate electrode 6 and the center of each optical device electrode 14 are normally displaced from each other by 10 μm or more due to positional displacement at the mount time. Subsequently, the atmospheric air around the silicon substrate 5 and the optical device 13 is replaced by nitrogen to reduce the oxygen concentration to 10 ppm or less.

When the silicon substrate 5 is heated by the heater 8 to melt the solder bumps 12 as shown in Fig. 2B, the melted solder bumps 12 start to deform to the shape in which the surface area is minimized by the surface tension. As a result, the optical device 13 is shifted due to the deformation of the solder bumps 12, and the optical device 13 stops its movement at the position where the center of each substrate electrode 6 is coincident with

the center of each optical device electrode 14. Thereafter, the silicon substrate 5 is cooled to solidify the solder bumps 12, whereby the optical device 13 is fixed with high positioning precision. The positional displacement amount between the center 5 of the optical device electrode 14 and the center of the substrate electrode 6 at this time is equal to about $2\mu\text{m}$ or less.

A first problem of the conventional method of mounting such a device as the optical device as described above or the like resides in that an atmosphere of an extremely low oxygen concentration is required. It takes a long time to reduce the oxygen concentration of the atmosphere to 10 ppm or less, and also a device for controlling the oxygen concentration is very expensive. The reason for this is as follows. That is, in order 15 to melt solder in the air, flux must be used or the oxygen concentration must be reduced to an extremely low level. From the viewpoint of reliability, it is not preferable to use flux for a device such as an optical device or the like, and thus the oxygen concentration must be managed to be set to a very low level.

20 A second problem resides in that it is impossible to mount a large and heavy optical device with high positioning precision because the solder bumps are crushed flat. This is because any consideration of reducing the force applied to the melted solder bumps in accordance with the weight of the optical 25 device is not taken.

SUMMARY OF THE INVENTION

Therefore, an object of the present invention is to 30 provide a mount method of fixing a device such as an optical

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device or the like onto a substrate with high positioning precision without managing the oxygen concentration.

Another object of the present invention is to provide a mount method of fixing a device such as a large and heavy optical
5 device or the like with high positioning precision by minute solder bumps.

In order to attain the above objects, according to the present invention, a mount method for joining a device to a substrate with soldering is characterized in that the joint of
10 the device and the substrate through solder is performed in liquid.

In the above mount method, the joint based on the soldering is performed while ultrasonic vibration is applied to the solder through the liquid.

15 Further, in order to attain the above object, according to the present invention, a method of joining a substrate electrode formed on a substrate and a device electrode formed on a device to each other by soldering to mount the device on the substrate, is characterized by comprising the steps of: attaching
20 a solder piece to the substrate electrode; melting the solder piece in liquid to form a solder bump; matching the substrate electrode having the solder bump formed thereon with the device electrode and disposing the device so as to confront the substrate in the liquid; positioning the device electrode to the
25 substrate electrode by the surface tension of the melted solder bump when the solder bump is melted in the liquid to join the device electrode and the substrate electrode to each other; and then solidifying the solder bump.

In the above mount method, when the solder piece is
30 melted to form the solder bump, ultrasonic vibration is applied

to the solder piece through the liquid.

In the above mount method, when the solder bump is melted in the liquid to join the device electrode and the substrate electrode to each other, ultrasonic vibration is applied to the
5 solder bump through the liquid.

In the mount method of the present invention, the liquid is inactive to the device and the substrate, and further inactive to the solder.

In the mount method of the present invention, the device
10 is an optical device, and the substrate is a semiconductor substrate.

The mount method of the present invention contains a step of melting the solder in inactive liquid. More specifically, the mount method of the present invention contains a step of heating
15 the substrate and the device to be joined to each other by the solder while the substrate and the device are immersed in inactive liquid.

Further, the mount method of the present invention is characterized by containing a step of applying ultrasonic
20 vibration to the surface of the melted solder when the solder is melted, thereby breaking the oxide film on the surface of the melted solder. More specifically, the mount method of the present invention contains a step of transmitting the ultrasonic vibration generated by ultrasonic wave generator through the
25 inactive liquid serving as a medium to the surface of the melted solder.

According to the present invention, since the device such as an optical device or the like, the substrate and the solder are immersed in the liquid, the ultrasonic vibration can be
30 transmitted through the liquid to the melted solder. Therefore,

the oxide film on the surface of the melted solder can be broken. Further, since these elements are located in the liquid, no oxygen exists around the solder. Therefore, even when the solder is melted, the surface of the solder is not oxidized.

5 Accordingly, the surface tension of the solder can exhibit its maximum effect, and the automatic positioning and mounting operation using the surface tension of the solder can be performed. In addition, the adhesion performance of the solder is excellent.

10 Since the device such as the optical device or the like is immersed in the liquid, buoyancy occurs, and the device becomes substantially light. Therefore, buoyancy is applied to a large heavy device and thus it becomes light. Accordingly, it can be sufficiently supported by melted solder bump which is easily
15 crushed flat in the air. Therefore, such a large and heavy device that cannot be mounted in the air can be automatically positioned and mounted by using the surface tension of the solder.

BRIEF DESCRIPTION OF THE DRAWINGS

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Figs. 1A to 1C are diagrams showing a series of steps of a conventional method of mounting an optical device;

Figs. 2A to 2C are diagrams showing a series of steps of a conventional method of mounting an optical device;

25 Figs. 3A to 3C are diagrams showing a series of steps of a mount method according to the present invention;

Figs. 4A to 4C are diagrams showing a series of steps of a mount method according to the present invention; and

30 Fig. 5 is a diagram showing an example of a substrate and a device which are mounted according to the method of the present

invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

5 A preferred embodiment according to the present invention will be described hereunder with reference to the accompanying drawings.

 Figs. 3A to 3C and Figs. 4A to 4C are diagrams showing a series of steps for a device mount method of an embodiment
10 according to the present invention.

 Referring to Figs. 3A to 3C and Figs. 4A to 4C, circular substrate electrodes 6 of $10\mu\text{m}$ to $200\mu\text{m}$ in diameter to join an optical device 13 to the surface of a substrate 5 are formed on the substrate 5. A die 1 and a punch 2 are used to punch a solder
15 sheet 3 to form solder pieces 4. The solder pieces 4 are melted and deformed in shape to obtain solder bumps 12. Optical device electrodes 14 of $10\mu\text{m}$ to $200\mu\text{m}$ in diameter which are formed on the optical device 13 are to be joined to the substrate electrodes 6 through the solder bumps 12. An ultrasonic wave
20 generator 10 for generating ultrasonic vibration and a heater 8 are secured to a container 7. As the ultrasonic wave generator 10 may be used an ultrasonic wave generator attached to an ultrasonic cleaning machine which is sold in the market and used to clean eyeglasses or other members. The ultrasonic vibration 11
25 generated by the ultrasonic wave generator 10 is propagated through inactive liquid 9 serving as a medium filled in the container 7. The inactive liquid 9 is inactive liquid which has no effect on the optical device 13 and the substrate 5, and it may be formed of organic compound of perfluorotrialkylamine group
30 $(\text{C}_n\text{F}_{2n+1})_3\text{N}$ or the like. The inactive liquid 9 being used has a

boiling point higher than a melting point of the solder bump 12. Further, a solder material must be suitably selected so that the melting point thereof is lower than the boiling point of the inactive liquid 9. For example, a combination of bismuth-tin based solder BiSn (Bi 52wt%, Sn 48wt%) having a melting point of 138.5 °C and inactive liquid $(C_5F_{11})_3N$ having a boiling point of 215 °C may be considered.

Fig. 5 is a perspective view showing a product which is fabricated by the present invention.

Referring to Fig. 5, the circular substrate electrodes 6 formed on the substrate 5 and the circular optical device electrodes 14 formed on the optical device 13 are joined to each other through the solder bumps 12. These elements are positioned and fixed so that the center of each optical device electrode 14 is located just above the center of each substrate electrode 6.

Next, the steps of Figs. 3A to 3C and Figs. 4A to 4C will be described.

As shown in Fig. 3A, the solder sheet 3 is punched by the die 1 and the punch 2 to form the solder pieces 4. The solder pieces 4 thus formed are bumped against the substrate electrodes 6 formed on the substrate 5. As shown in Fig. 3B, the substrate 5 against which the solder pieces 4 are bumped is disposed at the bottom portion of the container 7 in which the inactive liquid 9 is filled. The substrate 5 is heated by the heater 8 to melt the solder pieces 4. At this time, the ultrasonic vibration 11 generated by the ultrasonic wave generator 10 minutely vibrates the surfaces of the melted solder pieces 4 through the inactive liquid 9 serving as medium and breaks the oxide films on the surfaces of the solder pieces 4. Therefore, as shown in Fig. 3C, the solder bumps 12 can be formed from the solder pieces 4

without using flux.

Subsequently, as shown in Fig. 4A, the optical device 13 is temporarily mounted on the solder bumps 12 thus formed. At this time, the optical device 13 is temporarily mounted so that the center of each electrode 14 formed on the optical device 13 is located just above the center of each electrode 6 formed on the substrate 5, however, the positional displacement of 10 μ m or more occurs normally due to dispersion of the mount precision. Subsequently, as shown in Fig. 4B, the substrate 5 is heated by the heater 8 to melt the solder bumps 12, and at the same time the ultrasonic wave 11 generated by the ultrasonic wave generator 10 is propagated through the inactive liquid 9 to minutely vibrate the surfaces of the solder bumps 12. Therefore, the oxide films on the surfaces of the melted solder bumps 12 are broken and the melted solder exhibits its surface tension at maximum. Therefore, the melted solder starts to deform to such a shape that the surface area thereof is minimum, and the optical device 13 is also moved due to the deformation.

At this time, as shown in Fig. 4C, the optical element 13 moves to the position where the center of each optical device electrode 14 is located just above the center of each substrate electrode 6, and stopped there. Thereafter, the solder bumps 12 are cooled and solidified to fix the optical device 13 to the substrate 5.

In the present invention, as the device a semiconductor device may be used, and as the substrate a substrate for mounting an electric element such as a ceramic substrate, printed circuit board, and the like may be used.

A first effect of the present invention resides in that the solder between the device and the substrate can be performed

with no management of oxygen concentration. This is because the substrate and the device are immersed in liquid and the ultrasonic vibration is applied to the surface of the solder.

A second effect of the present invention resides in that
5 even a heavy device can be automatically positioned and mounted by using the surface tension of solder. This is because the positioning and mounting operation is carried out in liquid and thus the force applied to the melted solder is reduced by the amount corresponding to the buoyancy applied to the device and
10 the melted solder.

A third effect of the present invention resides in that no flux is required for soldering. This is because the solder is melted in liquid and ultrasonic vibration is applied to the surface of the solder.
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WHAT IS CLAIMED IS:

1. A mount method for joining a device to a substrate with soldering, characterized in that joint of said device and
5 said substrate through solder is performed in liquid.
2. The mount method as claimed in claim 1, wherein the joint based on said solder is performed while ultrasonic vibration is applied to said solder through the liquid.
- 10 3. The mount method as claimed in claim 1, wherein the liquid is inactive to said device and said substrate.
4. The mount method as claimed in claim 1, wherein said
15 device is an optical device.
5. The mount method as claimed in claim 1, wherein said device is a semiconductor device.
- 20 6. The mount method as claimed in claim 1, wherein said substrate is a semiconductor substrate.
7. The mount method as claimed in claim 1, wherein said substrate is a substrate for mounting an electric element.
- 25 8. The mount method as claimed in claim 1, wherein said substrate is a ceramic substrate.
9. The mount method as claimed in claim 1, wherein said
30 substrate is a printed circuit board.

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10. A method of joining a substrate electrode formed on a substrate and a device electrode formed on a device to each other by solder to mount the device on the substrate, comprising the steps of:

- 5 attaching a solder piece to the substrate electrode;
 melting the solder piece in liquid to form a solder bump;
 matching the substrate electrode having the solder bump
 formed thereon with the device electrode and disposing the device
 so as to confront the substrate in the liquid;
- 10 positioning the device electrode to the substrate
 electrode by surface tension of the melted solder bump when the
 solder bump is melted in the liquid to join the device electrode
 and the substrate electrode to each other; and then
 solidifying the solder bump.
- 15 11. The method as claimed in claim 10, wherein when the
 solder piece is melted to form the solder bump, ultrasonic
 vibration is applied to the solder piece through the liquid.
- 20 12. The method as claimed in claim 10, wherein when the
 solder bump is melted in the liquid to join the device electrode
 and the substrate electrode to each other, ultrasonic vibration
 is applied to the solder bump through the liquid.
- 25 13. The method as claimed in claim 10, wherein the liquid
 is inactive to said solder, said device and said substrate.
14. The method as claimed in claim 10, wherein said
 device is an optical device.

15. The method as claimed in claim 10, wherein said device is a semiconductor device.

16. The method as claimed in claim 10, wherein said
5 substrate is a semiconductor substrate.

17. The method as claimed in claim 10, wherein said substrate is a substrate for mounting an electric element.

10 18. The method as claimed in claim 10, wherein said substrate is a ceramic substrate.

19. The method as claimed in claim 10, wherein said substrate is a printed circuit board.

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ABSTRACT OF THE DISCLOSURE

When a substrate electrode (6) formed on a semiconductor
substrate (5) and a device electrode (14) formed on an optical
5 device (13) are joined to each other by solder to mount the
device (13) on the substrate (5), a solder piece attached to the
substrate electrode (6) is melted in inactive liquid (9) to form
a solder bump (12). The substrate electrode (6) on which the
solder bump (12) is formed and the device electrode (14) are
10 matched with each other, and the device (13) is disposed so as to
confront the substrate (5) in the inactive liquid (9).
Thereafter, when the solder bump (12) is melted in the inactive
liquid (9) and ultrasonic vibration (11) is applied to join the
device electrode (6) and the substrate electrode (14) to each
15 other, the positioning of the device electrode (14) to the
substrate electrode (6) is performed by the surface tension of
the melted solder bump (12), and then the solder bump (12) is
solidified.

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FIG. 1A
PRIOR ART

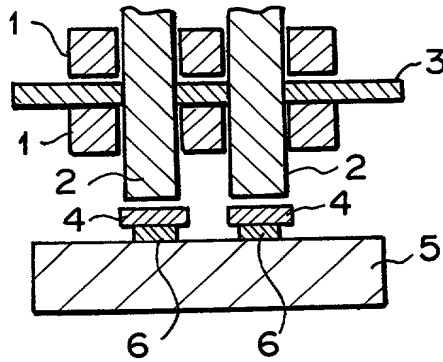


FIG. 1B
PRIOR ART

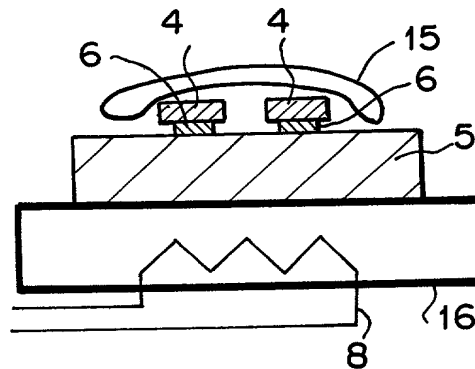


FIG. 1C
PRIOR ART

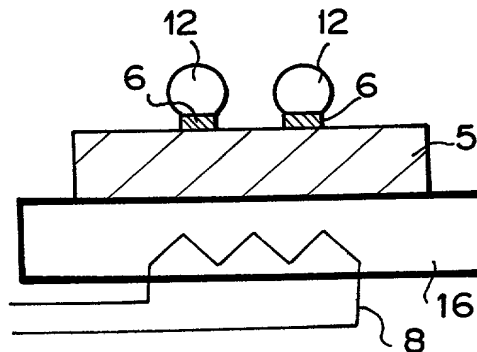


FIG. 2A
PRIOR ART

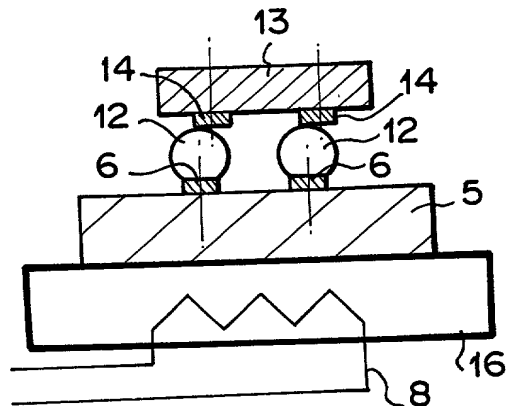


FIG. 2B
PRIOR ART

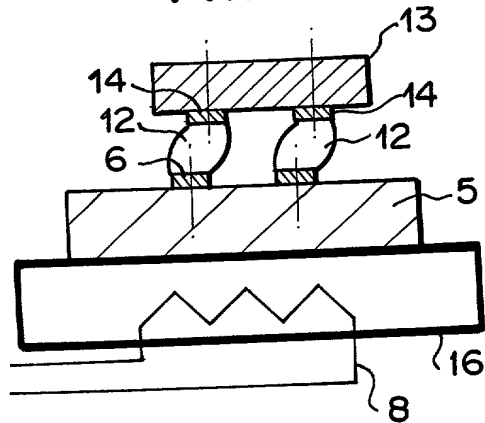


FIG. 2C
PRIOR ART

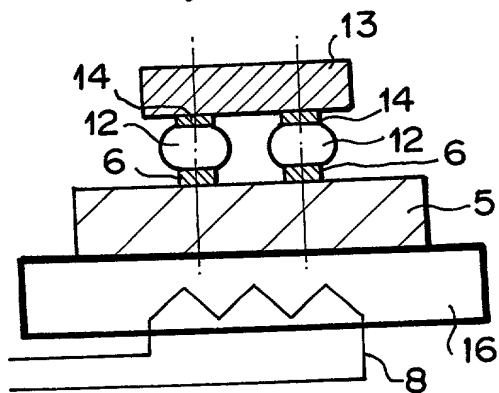


FIG. 3A

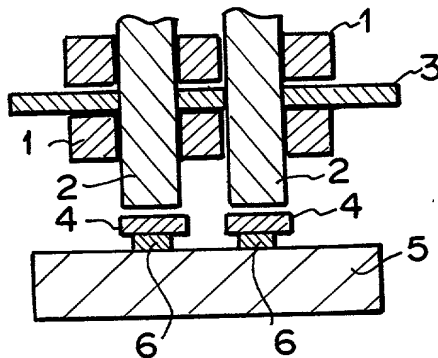


FIG. 3B

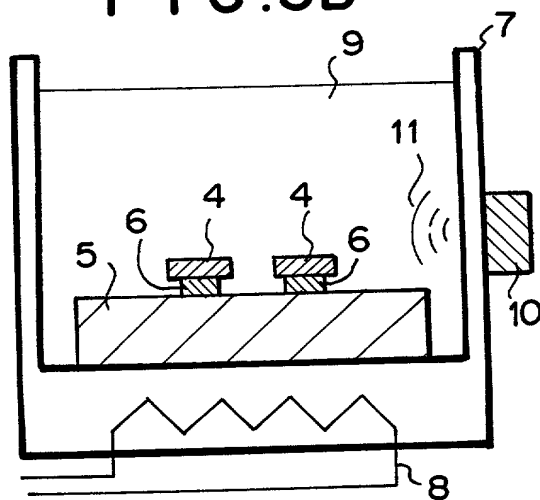


FIG. 3C

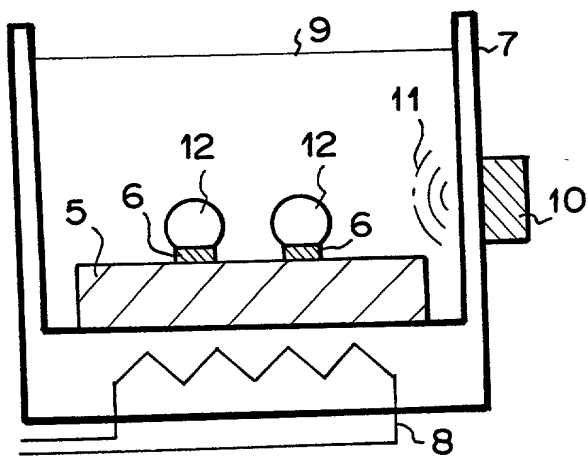


FIG. 4A

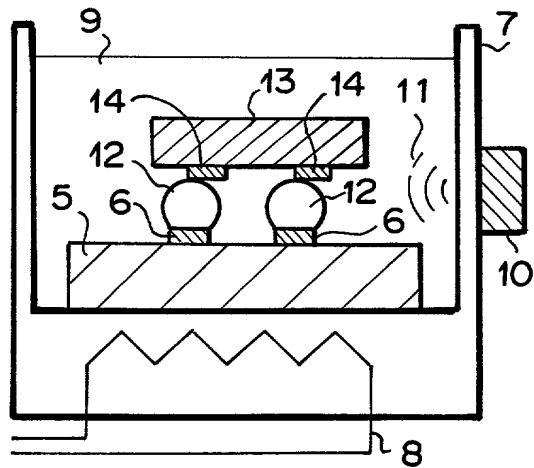


FIG. 4B

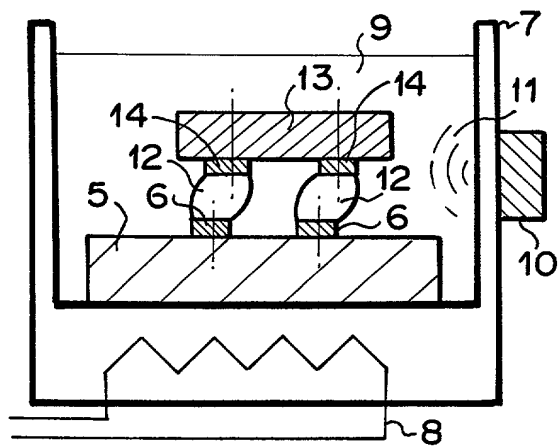


FIG. 4C

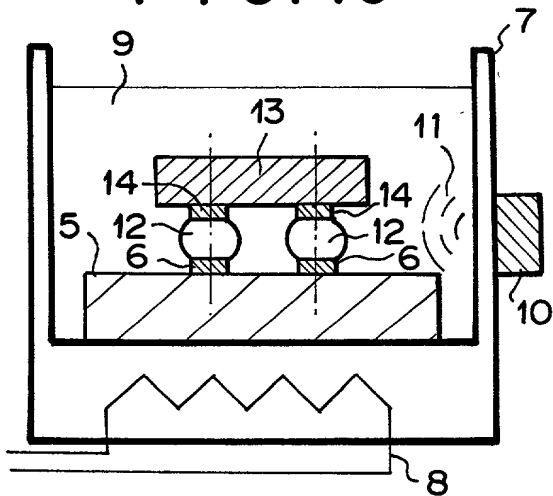
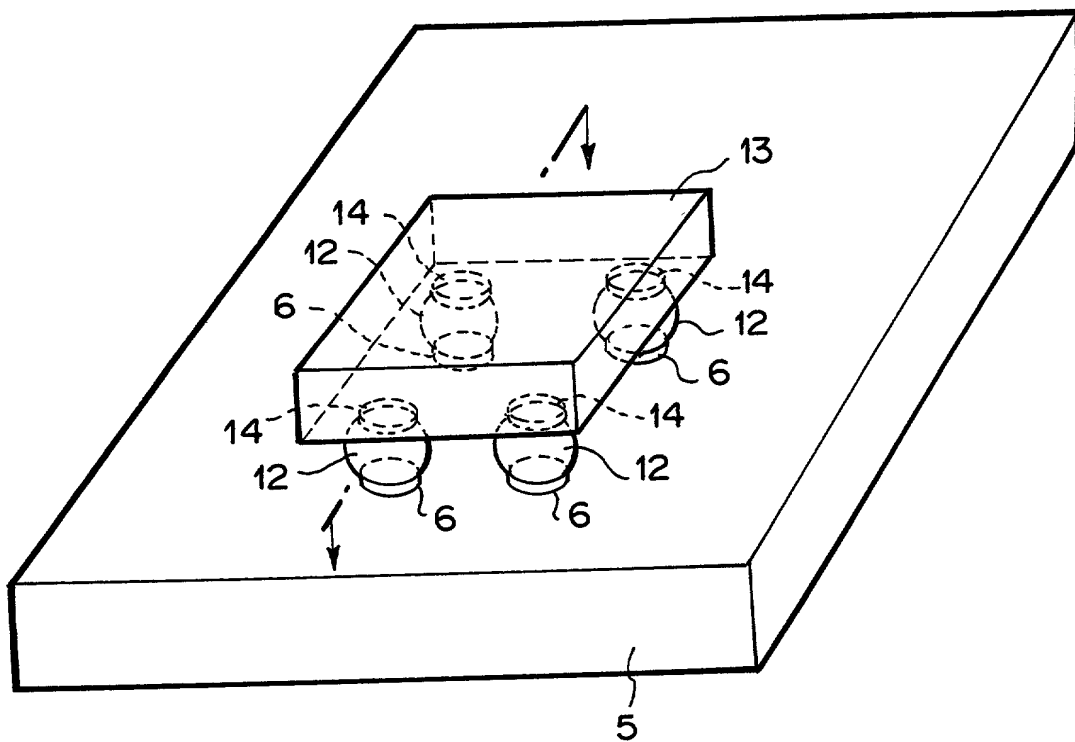


FIG. 5



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<p>As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are named) of the subject matter which is claimed and for which a patent is sought on the invention entitled:</p> <p style="text-align: center;"><u>DEVICE MOUNTING METHOD</u></p>			
<p>the specification of which is attached hereto, unless the following box is checked:</p> <p><input type="checkbox"/> was filed on _____ as United States patent Application Number or PCT International patent application number _____ and was amended on _____ (if any).</p>			
<p>I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.</p> <p>I acknowledge the duty to disclose all information known to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56.</p> <p>I hereby claim priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate or United States provisional application(s) listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:</p>			
Prior Foreign or Provisional Application(s)			
COUNTRY	APPLICATION NUMBER	DATE OF FILING (day, month, year)	PRIORITY CLAIMED UNDER 35 U.S.C. 119
Japan	326154/1997	27, 11, 1997	YES <u>X</u> NO _____
			YES _____ NO _____
			YES _____ NO _____
<p>I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.</p>			
UNITED STATES APPLICATION NUMBER	DATE OF FILING (day, month, year)	STATUS (patented, pending, abandoned)	
<p>I hereby appoint customer no. 2352 OSTROLENK, FABER, GERB & SOFFEN, LLP, and the members of the firm, Samuel H. Weiner - Reg. No. 18,510; Jerome M. Berliner - Reg. No. 18,653; Robert C. Faber - Reg. No. 24,322; Edward A. Meilman - Reg. No. 24,735; Stanley H. Lieberstein - Reg. No. 22,400; Steven I. Weisburd - Reg. No. 27,409; Max Moskowitz - Reg. No. 30,576; Stephen A. Soffen - Reg. No. 31,063; James A. Finder - Reg. No. 30,173; William O. Gray, III - Reg. No. 30,944; Louis C. Dujmich - Reg. No. 30,625 and Douglas A. Miro - Reg. No. 31,643, as attorneys with full power of substitution and revocation to prosecute this application, to transact all business in the Patent & Trademark Office connected therewith and to receive all correspondence.</p>			
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OSTROLENK, FABER, GERB & SOFFEN, LLP 1180 AVENUE OF THE AMERICAS NEW YORK, NEW YORK 10036-8403 CUSTOMER NO. 2352		(212) 382-0700	
<p>I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.</p>			
FULL NAME OF SOLE OR FIRST INVENTOR	INVENTOR'S SIGNATURE	DATE	
YOSHINOBU KANEYAMA	<i>Yoshinobu Kaneyama</i>	Nov. 16, 1998	
RESIDENCE (City and either State or Foreign Country)	COUNTRY OF CITIZENSHIP		
Tokyo, Japan	Japan		
POST OFFICE ADDRESS			
c/o NEC Corporation, 7-1, Shiba 5-chome, Minato-ku, Tokyo, Japan			
FULL NAME OF SECOND JOINT INVENTOR (IF ANY)	INVENTOR'S SIGNATURE	DATE	
RESIDENCE (City and either State or Foreign Country)	COUNTRY OF CITIZENSHIP		
POST OFFICE ADDRESS			
FULL NAME OF THIRD JOINT INVENTOR (IF ANY)	INVENTOR'S SIGNATURE	DATE	
RESIDENCE (City and either State or Foreign Country)	COUNTRY OF CITIZENSHIP		
POST OFFICE ADDRESS			

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